Mul Assembler Instruction

Read/Download
As a reference manual for the LLVM assembly language, this document covers various aspects including the AVR Assembler, instructions like ADD, SUB, MUL, DIV, REM, NEG, and their encoding. The ADD instruction and its comparison with other instructions are discussed, along with the use of labels in assembly code. The document also mentions the availability of a parts list, connections, and assembly instructions for download. It further addresses the possibility of performing multiplication or division without using MUL or DIV instructions due to their high CPU cycles requirement. An optimization guide for x86 instruction tables, lists of instruction latencies, throughputs, and micro-operations is also referenced. The text includes a mention of a website for math-related instructions, 16x16 bit from Ravi Pailoor, and an email address for Multicom, Inc.